

FIG. 1
(PRIOR ART)

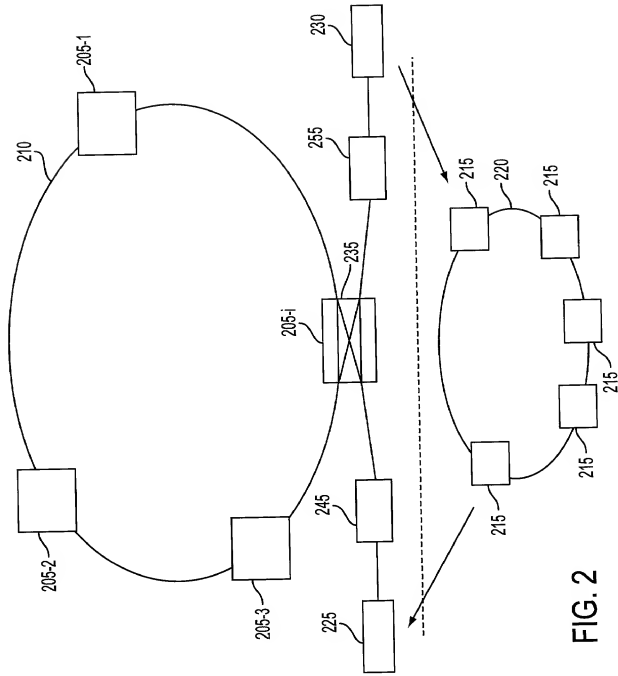
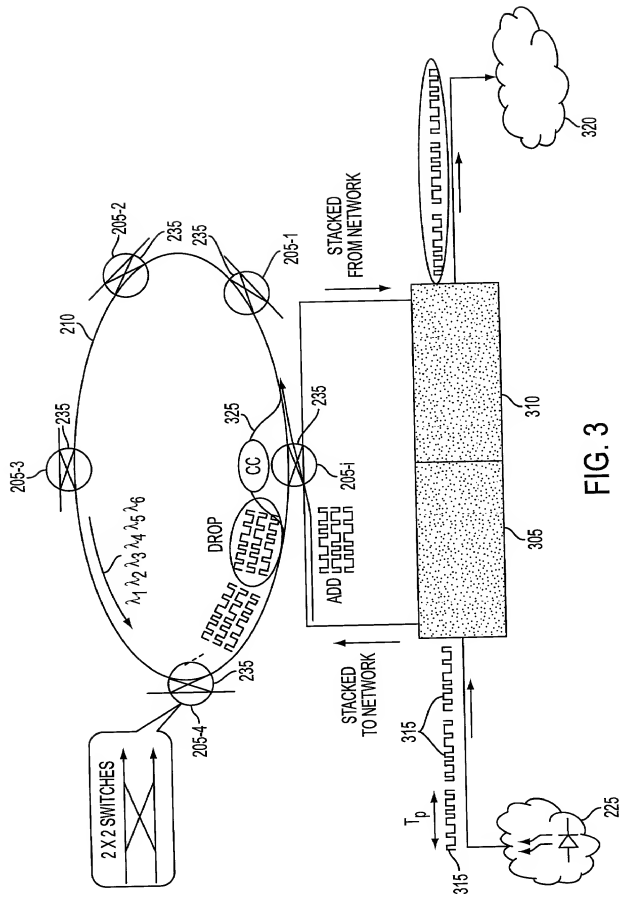


FIG. 2



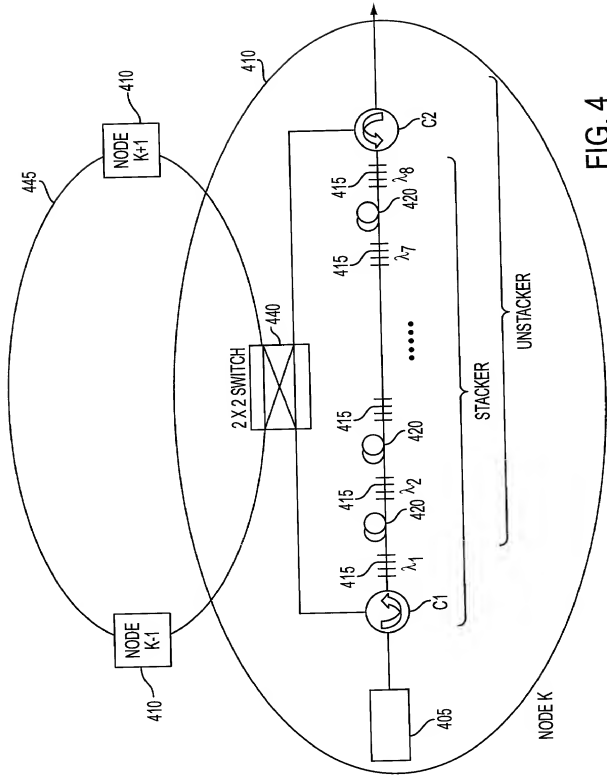


FIG. 4

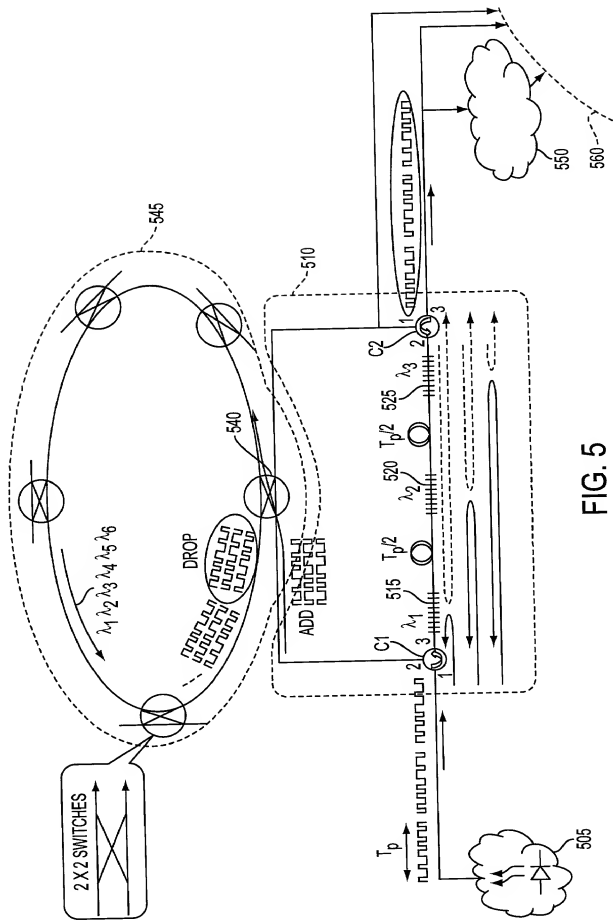


FIG. 5

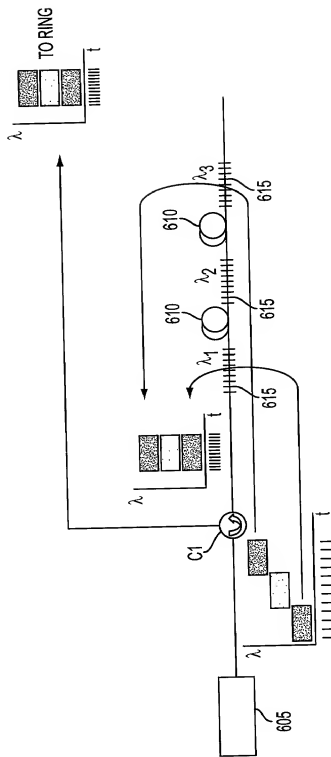


FIG. 6

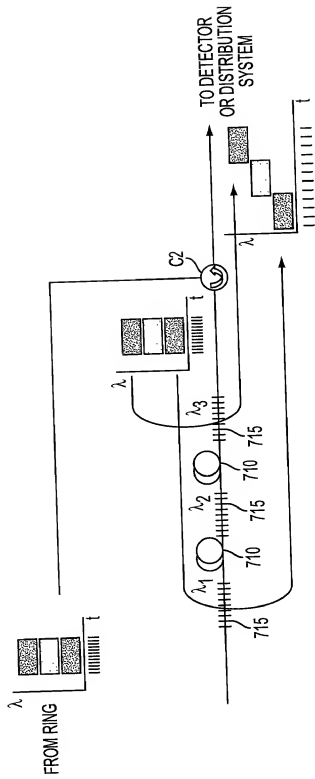


FIG. 7

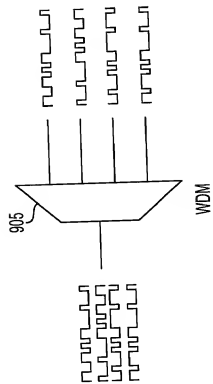


FIG. 8

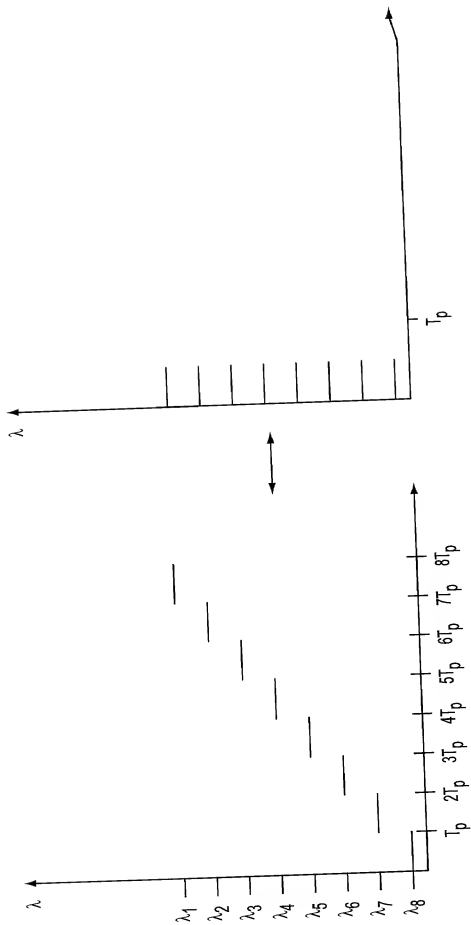


FIG. 9

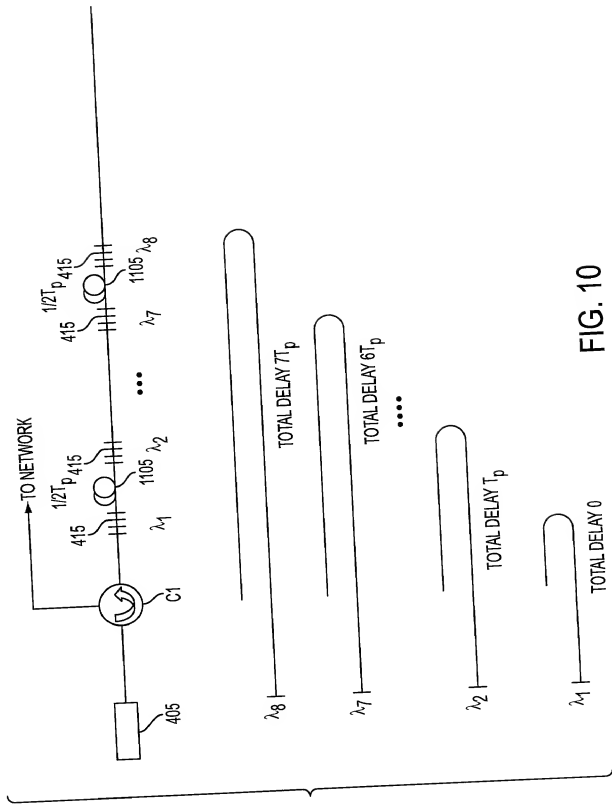


FIG. 10

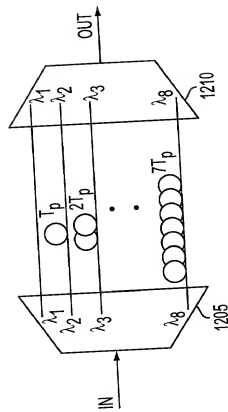


FIG. 11

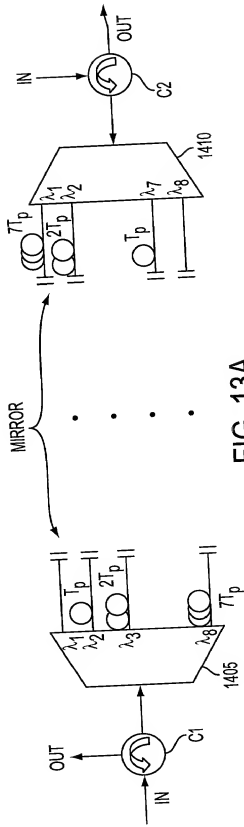


FIG. 13A

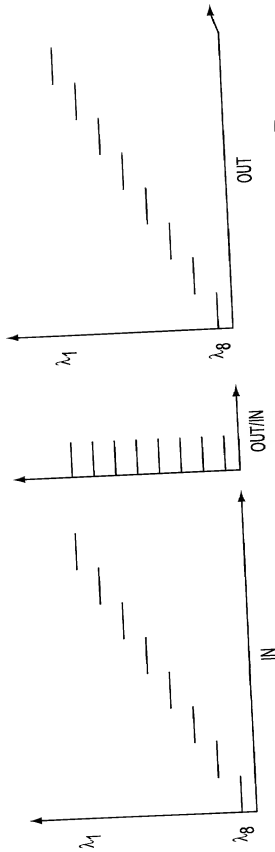


FIG. 13D

FIG. 13C

FIG. 13B

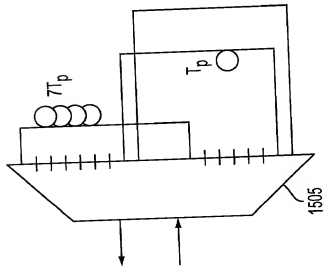


FIG. 14A

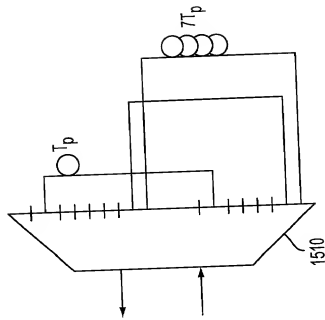


FIG. 14B

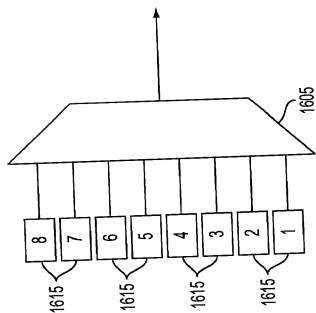


FIG. 15A

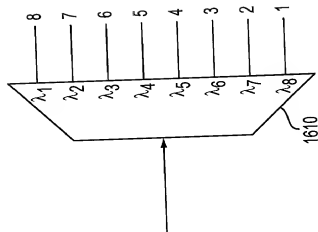


FIG. 15B

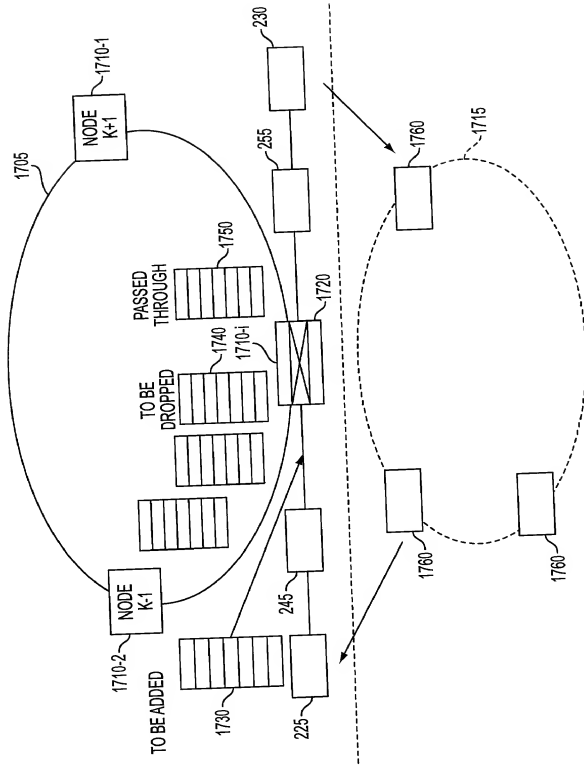


FIG. 16

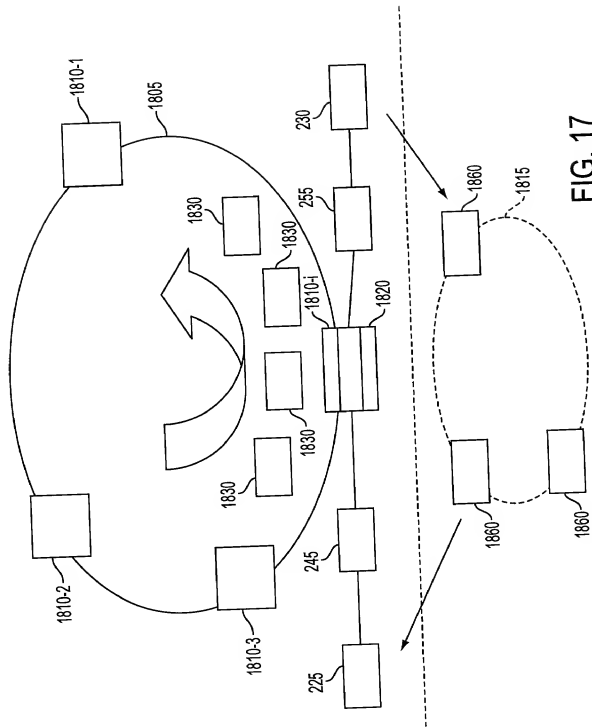


FIG. 17

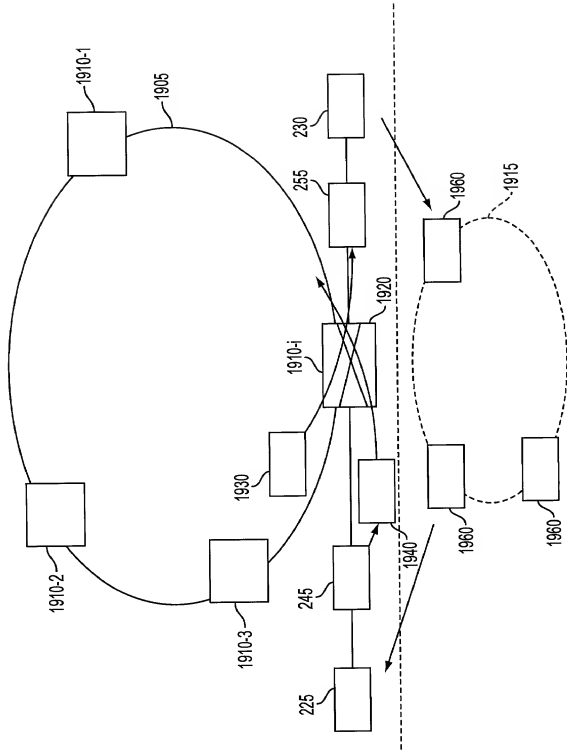


FIG. 18

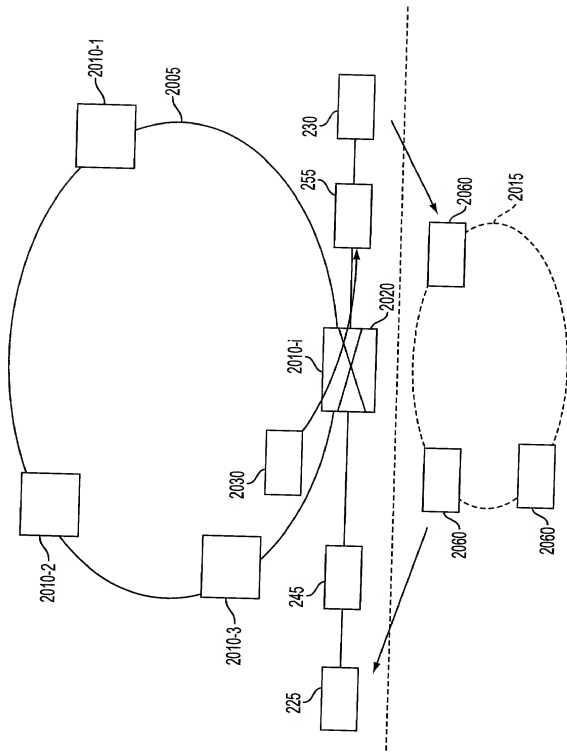


FIG. 19

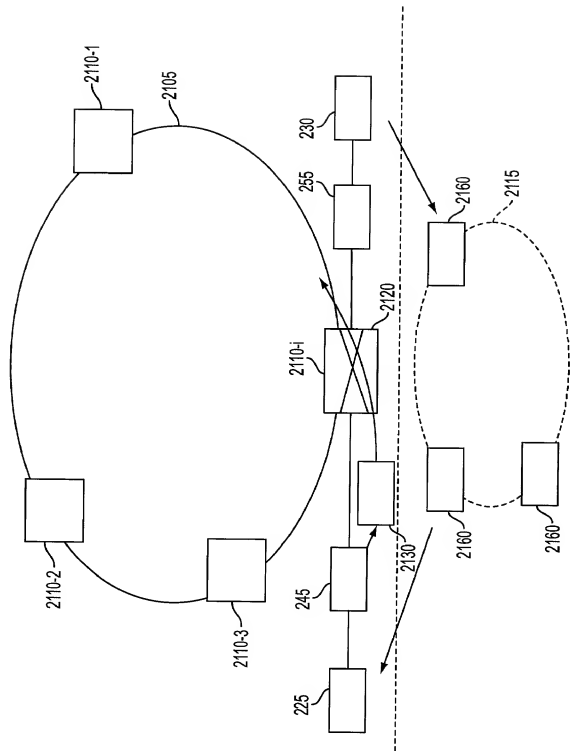
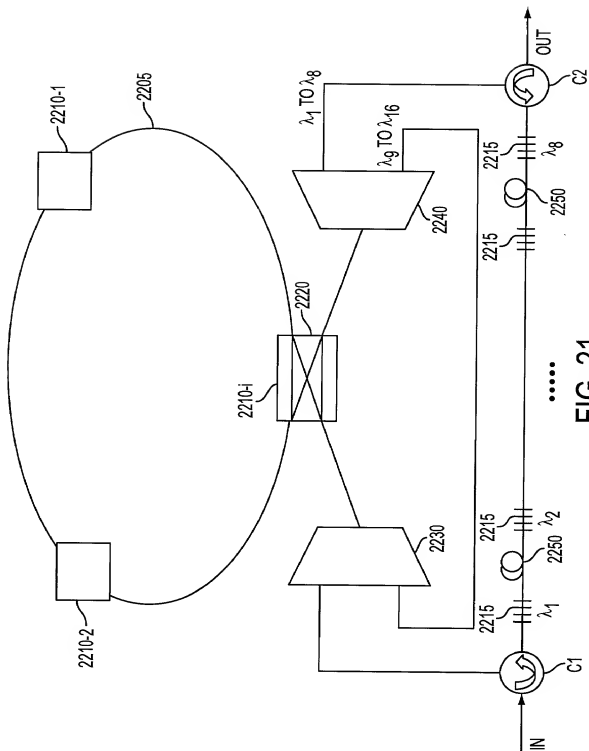


FIG. 20



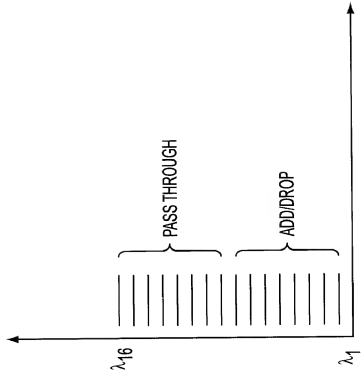


FIG. 22

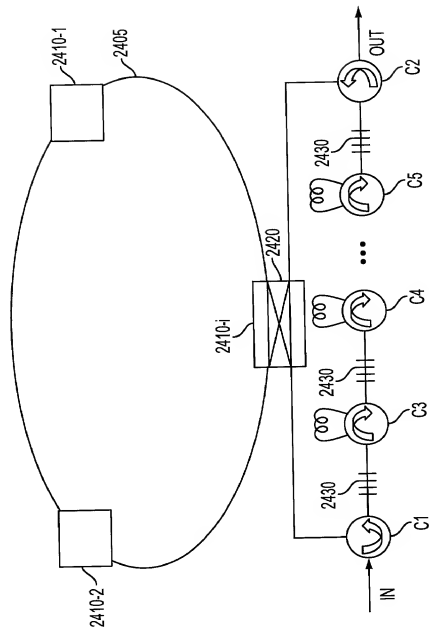


FIG. 23

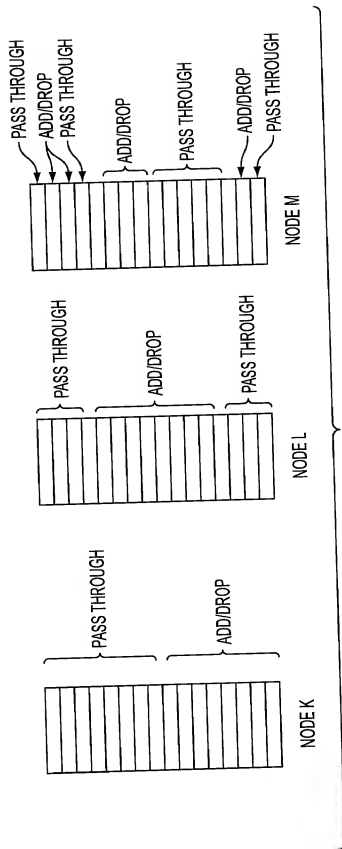


FIG. 24

FIG. 25

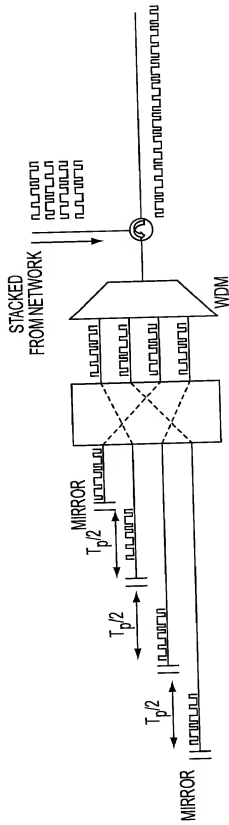


FIG. 26A

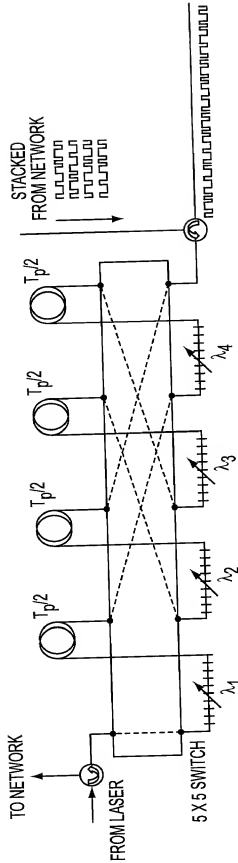


FIG. 26B

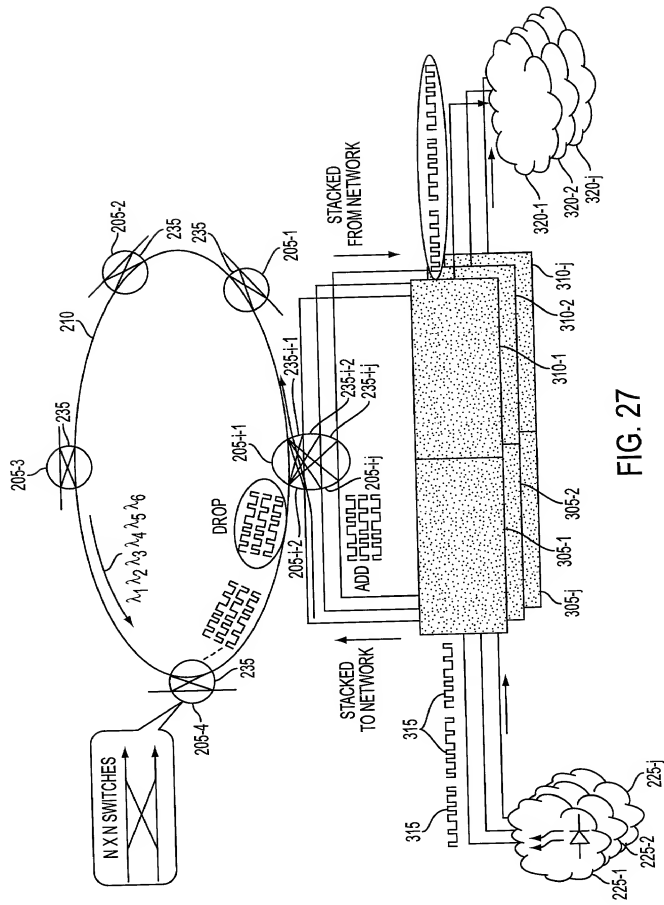


FIG. 27

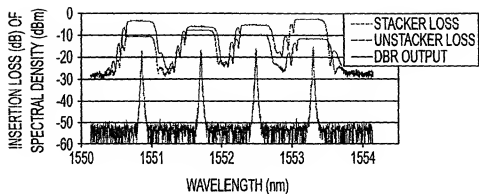


FIG. 28

(1) DBR WAVELENGTH
TUNING VOLTAGE



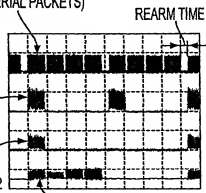
(2) INPUT PACKET MODULATION
TO THE DBR LASER

FIG. 29A

(1) DBR LASER INPUT AT
NODE 1 (SERIAL PACKETS)

(2) COMPOSITE
(STACKED)
PACKETS AT
NODE 1 OUTPUT

(3) DROPPED
COMPOSITE
PACKETS AT NODE 2



(4) RE-SERIALIZED PACKETS AT NODE 2

FIG. 29B

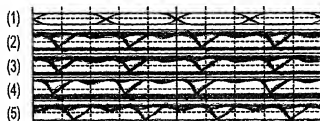


FIG. 30